

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Yoshitsugu MORITA et al. Confirmation No.: 6909
Serial No.: 10/584,032
Filed: June 22, 2006
Group Art Unit: 2812
Examiner: Patel, Reema
For: SEMICONDUCTOR DEVICE AND METHOD OF
MANUFACTURING THEREOF

RESPONSE

Mailstop AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Dear Sir:

In response to the Office Action from the Examiner dated May 8, 2009, please consider the accompanying remarks. A complete listing of all claims pending in the application is included for the Examiner's convenience. This Response is timely filed; thus, it is believed that no fees are presently due.